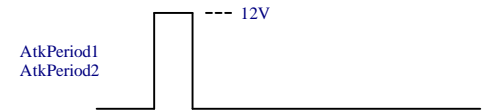
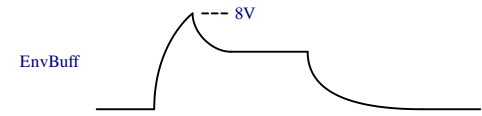
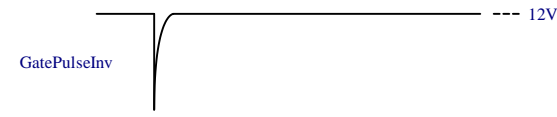
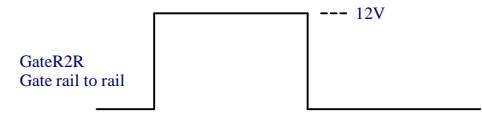
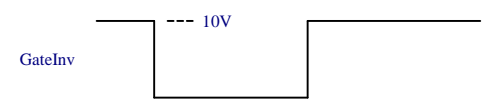
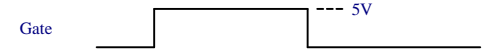
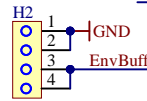
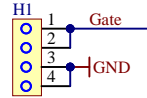
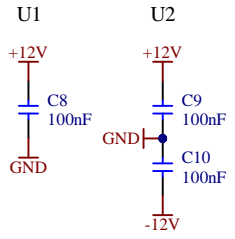
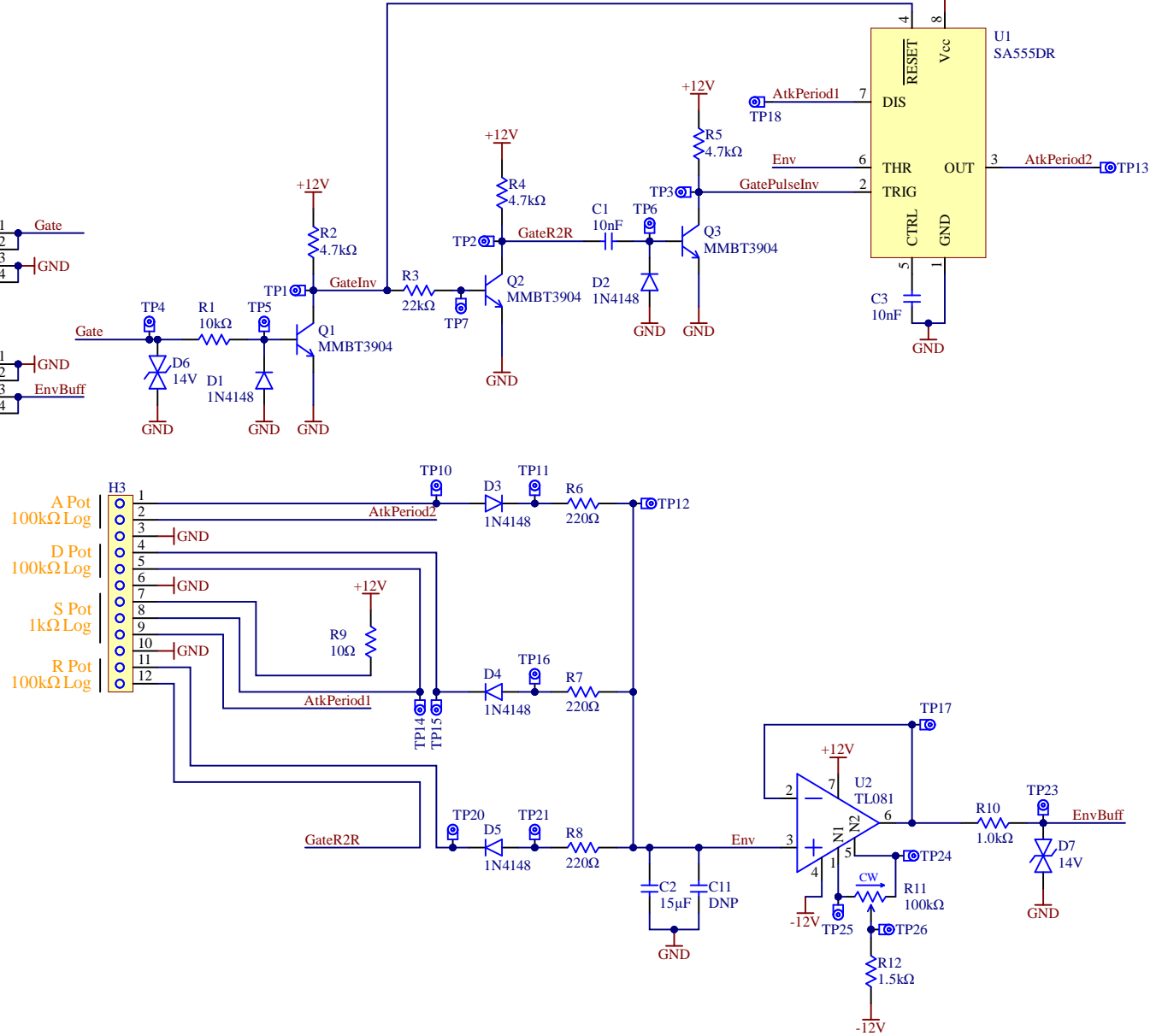
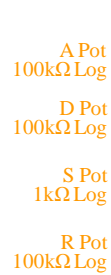


Decoupling Caps



All signals: Vmin = 0V



Derived from an ADSR design by Rene Schmitz from 1999
schmitzbits.de/adsr.html

	Project: ADSR	Date: 20241012
	PCB: ADSR MB	Rev: 2.0
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